

DC Brushless Motor Drivers

Three-phase Full-wave DC Brushless Fan Motor Driver

BD6345FV

General description

BD6345FV is a three-phase sensor-less fan that suit for speed controllable fans.

Its feature is sensor-less drive which doesn't require a hall device as a location detection sensor. Furthermore, introducing a PWM soft switched driving mechanism achieves silent operations and low vibrations.

Features

- Sensor-less drive
- Lock protection and automatic restart
- Rotating speed pulse signal (SOUT) output



Application

■ For 12V fan for general consumer equipment

•Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Supply voltage	V _{CC}	20	V
Power dissipation	Pd	1200* ¹	mW
Storage temperature	T _{stg}	-55 to +150	°C
Operating temperature	T _{opr}	-40 to +100	°C
Output voltage	V _{omax}	20	V
Output Current	I _{omax}	1.2* ²	Α
SOUT signal output voltage	V _{SOUT}	20	V
SOUT signal output current	I _{SOUT}	10	mA
REF current ability	I _{REF}	8	mA
Input voltage (TOSC)	V _{IN}	6.5	V
Junction temperature	Tjmax	150	°C

*1 Reduce by 9.6mW/°C over Ta=25°C (on 70.0mm×70.0mm×1.6mm glass epoxy board)

*2 T not exceed Pd and ASO

*2 It is permissible to 1.5A, 1 or less second.

Recommended operating conditions

Parameter	Symbol	Limit	Unit
Operating supply voltage range	Vcc	5.5 to 17.0	V

OProduct structure : Silicon monolithic integrated circuit OThis product is not designed protection against radioactive rays



Pin configuration



Fig.1 Pin configuration

Pin description

11	in description						
	P/No.	T/Name	Function				
	1	GND	GND terminal				
	2	GND	GND terminal				
	3	GND	GND terminal				
	4	REF	Reference voltage terminal				
	5	TEST1	TEST terminal				
	6	TEST2	TEST terminal				
	7	N.C.					
	8	N.C					
	9	U	Motor output U				
	10	PGND	Motor GND terminal				
	11	V	Motor output V				
	12	W	Motor output W				
	13	VCC	Power Supply terminal				
	14	COM	Motor central tap terminal				
	15	TOSC	Oscillating capacitor connecting terminal for synchronous driving				
	16	N.C					
	17	SOUT	Rotating speed pulse signal output terminal				
	18	GND	GND terminal				
	19	GND	GND terminal				
	20	GND	GND terminal				

Block diagram



Fig.2 Block diagram

●Electrical characteristics(Unless otherwise specified Ta=25°C, Vcc=12V)

		Limit				
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Circuit current	I _{CC}	4	7	10	mA	
<ref></ref>						
REF voltage	VREF	4.8	5	5.2	V	I _{REF} = -2mA
<tosc></tosc>						
TOSC high voltage	V _{TOSCH}	2.3	2.5	2.7	V	
TOSC low voltage	V _{TOSCL}	0.8	1.05	1.2	V	
TOSC Charge current	Ictosc	-80	-60	-40	uA	
TOSC Discharge current	IDTOSC	40	60	80	uA	
<sout output=""></sout>						
SOUT low voltage	V _{SOUTL}	-	0.3	0.4	V	I _{SOUT} =5mA
SOUT leak current	ISOUTL	-	-	10	uA	V _{SOUT} =20V
<lock protection=""></lock>						
Lock detect ON time	T _{ON}	0.3	0.5	0.8	S	TOSC=2200pF
Lock detect OFF time	T _{OFF}	3	5	8	s	
<output></output>						
Output Libratia as	V	-	0.15	0.20	V	Io=-200mA
Output Hi voltage	V _{он}					(V _{CC} common)
	V		0.00	0.16	V	lo=200mA
Output Lo voltage	V _{OL}	-	0.09	0.16	v	(GND common)

About this specification, it is a provisional spec , and there is a possibility of the change.

Typical performance curves(Reference data)



Fig.6 TOSC High/Low voltage

Typical performance curves(Reference data)





Typical performance curves(Reference data)





Fig.13 Output Lo voltage (Vcc=12V)

Fig.14 Output Lo voltage (Ta=25°C)

Application circuit example(Constant values are for reference)



Fig.15 Application circuit

Substrate design note

- a) IC power, motor outputs, and motor ground lines are made as fat as possible.
- b) IC ground (signal ground) line is common with the application ground except motor ground, and arranged near to (-) land.
- c) The bypass capacitor and/or Zenner diode are arrangement near to Vcc terminal.

Description of operations

1) Sensorless Drive

BD6345FV is a motor driver IC for driving a three-phase brushless DC motor without a hall sensor. Detecting a rotor location firstly at startup, an appropriate logic for the rotation direction is obtained using this information and given to each phase to rotate the motor. Then, the rotation of the motor induces electromotive voltage in each phase wiring and the logic based on the induced electromotive voltage is applied to the each phase to continue rotating.

2) Motor output U,V,W and FG output signals

In Fig.16, the timing charts of the output signals from the U, V and W phases as well as the SOUT terminal is shown. Assuming that a three-slot tetrode motor is used, two pulse outputs of SOUT are produced for one motor cycle. The three phases are excited in the order of U, V and W phases.



Fig.16 sensor-less drive

Output pottorn	Motor output			
Output pattern	Motor output U	Motor output V	Motor output W	
1	Н	L	Hi-Z	
2	Н	Hi-Z	L	
3	Hi-Z	Н	L	
4	L	Н	Hi-Z	
5	L	Hi-Z	Н	
3	Hi-Z	L	Н	

* About the output pattern, It changes in the flow of " $1 \rightarrow 2 \rightarrow 3 \sim 6 \rightarrow 1$ ".

3) Lock Protection Feature, Automatic Recovery Circuit

To prevent passing a coil current on any phase when a motor is locked, it is provided with a function, which can turn OFF the output for a certain period of time and then automatically restore itself to the normal operation. During the motor rotation, an appropriate logic based on the induced electromotive voltage can be continuously given to each phase ; on the other hand, when the motor is locked, no induced electromotive voltage is obtained. Utilizing this phenomenon to take a protective against locking, when the induced electromotive voltage is not detected for a predetermined period of time (TON), it is judged that the motor is locked and the output is turned OFF for a predetermined period of time (TOFF). In Fig.17, the timing chart is shown.





4) SOUT signal mask time when power supply is turned on SOUT signal is masked at start operation. When supply is turned on, SOUT signal is fixed Hi between 0.6sec. SOUT signal operates usually after 0.6 sec.



Fig.18 SOUT operation at start

5) UVLO (Under voltage lock out circuit)

In the operation area under the guaranteed operating power supply voltage of 5.5V (typ.), the transistor on the output can be turned OFF at a power supply voltage of 3.9V (typ.). A hysteresis width of 250mV is provided and a normal operation can be performed at 4.15V(typ.). This function is installed to prevent unpredictable operations, such as a large amount of current passing through the output, by means of intentionally turning OFF the output during an operation at a very low power supply voltage which may cause an abnormal function in the internal circuit. About turning off a output voltage at UVLO, It becomes a OFF mode. (Upper MOS FET and Under MOS FET are turned OFF.)

6) Motor start up frequency setting

The TOSC terminal starts a self-oscillation by connecting a capacitor between the TOSC terminal and GND terminal. It becomes a start-up frequency, and synchronized time can be adjusted by changing external capacitor. When the capacitor value is small, synchronized time becomes short. It is necessary to choose the best capacitor value for optimum start-up operation.

Equivalent circuit

1) SOUT output terminal

2) Motor output terminal





3) Coil midpoint terminal





4) Reference voltage terminal

5) Oscillating capacitor connecting terminal



Safety measure

1) Reverse connection protection diode

Reverse connection of power results in IC destruction as shown in Fig.19. When reverse connection is possible, reverse connection protection diode must be added between power supply and V_{CC} .





- 2) Measure against V_{CC} voltage rise by back electromotive force
- Back electromotive force (Back EMF) generates regenerative current to power supply. However, when reverse connection protection diode is connected, V_{CC} voltage rises because the diode prevents current flow to power supply.



When you use reverse connection protection diode, Please connect Zenner diode. Do not exceed absolute maximum ratings Vcc=20V.



Fig.21 Measure against VCC voltage rise

- 3) Problem of GND line PWM switching
 - Do not perform PWM switching of GND line because GND terminal potential cannot be kept to a minimum.



Fig.22 GND line PWM switching prohibited

4) SOUT output

SOUT output is an open drain and requires pull-up resistor. Adding resistor R1 can protect the IC. An excess of absolute maximum rating, when SOUT output terminal is directly connected to power supply, could damage the IC.



Fig.23 Protection of SOUT terminal

5) Location of IC (Generally three-phase sensor less driver IC)

a) Generally, three-phase sensorless driver is rotated motor by detecting the induced electromotive voltage. Line noise, line resistance is influenced for detecting the induced electromotive voltage. From motor to IC line should be shorted,

its suggest that location of IC is on the board of Motor in below Fig.24..

b) In three-phase sensorless and variable speed driver, It is necessary to tuning motor and IC (each motor units). (Usually Motor maker does it to tuning motor and IC.)



Fig.24 Location of IC

6) Note for contents

To explain about function of operation, timing charts might be partly omitted.

Power dissipation

Power dissipation (total loss) indicates the power that can be consumed by IC at Ta=25°C (normal temperature). IC is heated when it consumes power, and the temperature of IC chip becomes higher than ambient temperature. The temperature that can be accepted by IC chip depends on circuit configuration, manufacturing process, etc, and consumable power is limited. Power dissipation is determined by the temperature allowed in IC chip (maximum junction temperature) and thermal resistance of package (heat dissipation capability). The maximum junction temperature is in general equal to the maximum value in the storage temperature range.

Heat generated by consumed power of IC is radiated from the mold resin or lead frame of package. The parameter which indicates this heat dissipation capability (hardness of heat release) is called heat resistance, represented by the symbol θja[°C/W]. This heat resistance can estimate the temperature of IC inside the package. Fig.25 shows the model of heat resistance of the package. Heat resistance θja, ambient temperature Ta, junction temperature Tj, and power consumption P can be calculated by the equation below:

$$\theta$$
 ja = (Tj-Ta) / P [°C/W]

Thermal derating curve indicates power that can be consumed by IC with reference to ambient temperature. Power that can be consumed by IC begins to attenuate at certain ambient temperature. This gradient is determined by thermal resistance θ_{ja} . Thermal resistance θ_{ja} depends on chip size, power consumption, package ambient temperature, packaging condition, wind velocity, etc., even when the same package is used. Thermal derating curve indicates a reference value measured at a specified condition. Fig.26 shows a thermal derating curve (Value when mounting FR4 glass epoxy board 70[mm]×1.6[mm] (copper foil area below 3[%]))



Operational Notes

1) Absolute maximum ratings

An excess in the absolute maximum rations, such as supply voltage, temperature range of operating conditions, etc., can break down the devices, thus making impossible to identify breaking mode, such as a short circuit or an open circuit. If any over rated values will expect to exceed the absolute maximum ratings, consider adding circuit protection devices, such as fuses.

- Connecting the power supply connector backward Connecting of the power supply in reverse polarity can damage IC. Take precautions when connecting the power supply lines. An external direction diode can be added.
- 3) Power supply line

Back electromotive force causes regenerated current to power supply line, therefore take a measure such as placing a capacitor between power supply and GND for routing regenerated current. And fully ensure that the capacitor characteristics have no problem before determine a capacitor value. (When applying electrolytic capacitors, capacitance characteristic values are reduced at low temperatures)

4) GND potential

It is possible that the motor output terminal may deflect below GND terminal because of influence by back electromotive force of motor. The potential GND terminal must be minimum potential in all operating conditions, except that the levels of the motor outputs terminals are under GND level by the back electromotive force of the motor coil. Also ensure that all terminals except GND and motor output terminals do not fall below GND voltage including transient characteristics. Malfunction may possibly occur depending on use condition, environment, and property of individual motor. Please make fully confirmation that no problem is found on operation of IC.

5) Thermal design

Use a thermal design that allows for a sufficient margin in light of the power dissipation (Pd) in actual operating conditions.

6) Inter-pin shorts and mounting errors

Use caution when positioning the IC for mounting on printed circuit boards. The IC may be damaged if there is any connection error or if pins are shorted together.

7) Actions in strong electromagnetic field

Use caution when using the IC in the presence of a strong electromagnetic field as doing so may cause the IC to malfunction.

8) ASO

When using the IC, set the output transistor so that it does not exceed absolute maximum rations or ASO.

9) Thermal shut down circuit

The IC incorporates a built-in thermal shutdown circuit (TSD circuit). Operation temperature is 175°C (typ.) and has a hysteresis width of 25°C (typ.). When IC chip temperature rises and TSD circuit works, the output terminal becomes an open state. TSD circuit is designed only to shut the IC off to prevent thermal runaway. It is not designed to protect the IC or guarantee its operation. Do not continue to use the IC after operation this circuit or use the IC in an environment where the operation of this circuit is assumed.

10) Testing on application boards

When testing the IC on an application board, connecting a capacitor to a pin with low impedance subjects the IC to stress. Always discharge capacitors after each process or step. Always turn the IC's power supply off before connecting it to or removing it from a jig or fixture during the inspection process. Ground the IC during assembly steps as an antistatic measure. Use similar precaution when transporting or storing the IC.

11) GND wiring pattern

When using both small signal and large current GND patterns, it is recommended to isolate the two ground patterns, placing a single ground point at the ground potential of application so that the pattern wiring resistance and voltage variations caused by large currents do not cause variations in the small signal ground voltage. Be careful not to change the GND wiring pattern of any external components, either.

12) Capacitor between output and GND

When a large capacitor is connected between output and GND, if VCC is shorted with 0V or GND for some cause, it is possible that the current charged in the capacitor may flow into the output resulting in destruction. Keep the capacitor between output and GND below 100uF.

13) IC terminal input

When VCC voltage is not applied to IC, do not apply voltage to each input terminal. When voltage above VCC or below GND is applied to the input terminal, parasitic element is actuated due to the structure of IC. Operation of parasitic element causes mutual interference between circuits, resulting in malfunction as well as destruction in the last. Do not use in a manner where parasitic element is actuated.

14) In use

We are sure that the example of application circuit is preferable, but please check the character further more in application to a part that requires high precision. In using the unit with external circuit constant changed, consider the variation of externally equipped parts and our IC including not only static character but also transient character and allow sufficient margin in determining.

•status of this document

The English version of this document is formal specification. A customer may use this translation version only for a reference to help reading the formal version.

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Physical Dimension Tape and Reel Information SSOP-B20



Marking Diagram



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