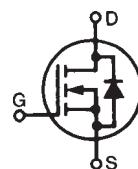


# PolarHV™ HiPerFET Power MOSFET (Electrically Isolated Back Surface)

**IXFC 36N50P**
**IXFR 36N50P**

$V_{DSS}$  = 500 V  
 $I_{D25}$  = 19 A  
 $R_{DS(on)}$  ≤ 190 mΩ  
 $t_{rr}$  ≤ 200 ns

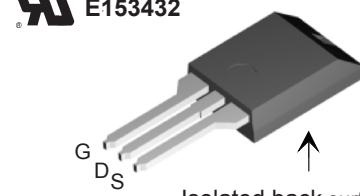
N-Channel Enhancement Mode  
Avalanche Rated  
Fast Intrinsic Diode



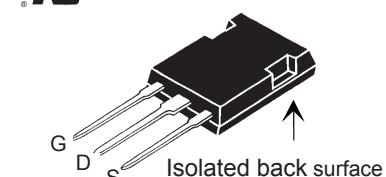
Symbol	Test Conditions	Maximum Ratings		
$V_{DSS}$	$T_J = 25^\circ C$ to $150^\circ C$	500	V	
$V_{DGR}$	$T_J = 25^\circ C$ to $150^\circ C$ ; $R_{GS} = 1 M\Omega$	500	V	
$V_{GSS}$	Continuous	± 30	V	
$V_{GSM}$	Transient	± 40	V	
$I_{D25}$	$T_c = 25^\circ C$	19	A	
$I_{DM}$	$T_c = 25^\circ C$ , pulse width limited by $T_{JM}$	100	A	
$I_{AR}$	$T_c = 25^\circ C$	36	A	
$E_{AR}$	$T_c = 25^\circ C$	50	mJ	
$E_{AS}$	$T_c = 25^\circ C$	1.5	J	
$dv/dt$	$I_s \leq I_{DM}$ , $di/dt \leq 100 A/\mu s$ , $V_{DD} \leq V_{DSS}$ , $T_J \leq 150^\circ C$ , $R_G = 4 \Omega$	20	V/ns	
$P_D$	$T_c = 25^\circ C$	156	W	
$T_J$		-55 ... +150	°C	
$T_{JM}$		150	°C	
$T_{stg}$		-55 ... +150	°C	
$T_L$	1.6 mm (0.062 in.) from case for 10 s	300	°C	
$V_{ISOL}$	50/60 Hz, RMS, 1 minute	2500	V~	
$F_c$	Mounting Force (IXFC) (IXFR)	11..65 / 2.5..15 20..120 / 4.5..25	N/lb	
Weight	(IXFC) (IXFR)	3 5	g	

Symbol	Test Conditions ( $T_J = 25^\circ C$ unless otherwise specified)	Characteristic Values		
		Min.	Typ.	Max.
$BV_{DSS}$	$V_{GS} = 0 V$ , $I_D = 250 \mu A$	500		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 4 mA$	2.5		V
$I_{GSS}$	$V_{GS} = \pm 30 V$ , $V_{DC} = 0$		± 100	nA
$I_{DSS}$	$V_{DS} = V_{DSS}$ $V_{GS} = 0 V$		25 250	μA
$R_{DS(on)}$	$V_{GS} = 10 V$ , $I_D = I_T$		190	mΩ

ISOPLUS220™ (IXFC)  
E153432



ISOPLUS247™ (IXFR)  
E153432



G = Gate      D = Drain  
S = Source

## Features

- International standard isolated packages
- UL recognized packages
- Silicon chip on Direct-Copper-Bond substrate
  - High power dissipation
  - Isolated mounting surface
  - 2500V electrical isolation
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
  - easy to drive and to protect
- Fast intrinsic diode

## Advantages

- Easy to mount
- Space savings
- High power density

Symbol	Test Conditions	Characteristic Values			
		(T <sub>J</sub> = 25°C unless otherwise specified)	Min.	Typ.	Max.
<b>g<sub>fs</sub></b>	V <sub>DS</sub> = 20 V; I <sub>D</sub> = I <sub>T</sub> , Note 1	23	35	S	
<b>C<sub>iss</sub></b> <b>C<sub>oss</sub></b> <b>C<sub>rss</sub></b>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz	5500	pF		
		510	pF		
		40	pF		
<b>t<sub>d(on)</sub></b> <b>t<sub>r</sub></b> <b>t<sub>d(off)</sub></b> <b>t<sub>f</sub></b>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 0.5 I <sub>D25</sub> R <sub>G</sub> = 4 Ω (External)	29	ns		
		23	ns		
		82	ns		
		23	ns		
<b>Q<sub>g(on)</sub></b> <b>Q<sub>gs</sub></b> <b>Q<sub>gd</sub></b>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 0.5 V <sub>DSS</sub> , I <sub>D</sub> = I <sub>T</sub>	93	nC		
		30	nC		
		31	nC		
<b>R<sub>thJC</sub></b>	(ISOPLUS 247)	0.15	°C/W		
<b>R<sub>thCS</sub></b>					
	(ISOPLUS 220)	0.21	°C/W		

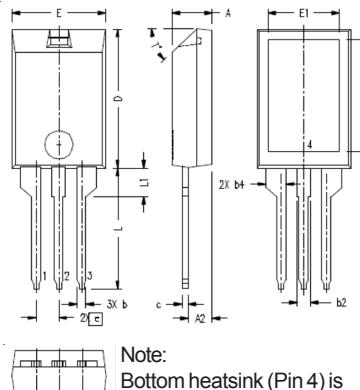
**Source-Drain Diode**
**Characteristic Values**

(T<sub>J</sub> = 25°C, unless otherwise specified)

Symbol	Test Conditions	Min.	Typ.	Max.
I <sub>s</sub>	V <sub>GS</sub> = 0 V		36	A
I <sub>SM</sub>	Repetitive		100	A
V <sub>SD</sub>	I <sub>F</sub> = I <sub>s</sub> , V <sub>GS</sub> = 0 V,		1.5	V
<b>t<sub>rr</sub></b> <b>I<sub>RM</sub></b>	I <sub>F</sub> = 25A, -di/dt = 100 A/μs V <sub>R</sub> = 100 V; V <sub>GS</sub> = 0 V	200	ns	
		8	A	
Q <sub>RM</sub>		0.6	μC	

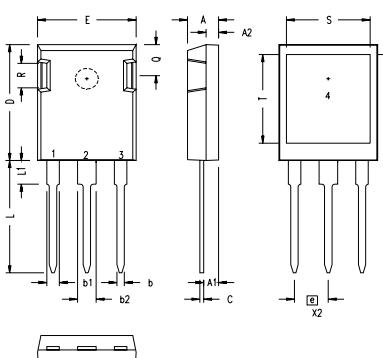
**Notes:**

1. Pulse test, t ≤ 300 μs, duty cycle d ≤ 2 %;
2. Test current I<sub>T</sub> = 18A.

**ISOPLUS220™ (IXFC) Outline**


SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.157	.197	4.00	5.00
A2	.098	.118	2.50	3.00
b	.035	.051	0.90	1.30
b2	.049	.065	1.25	1.65
b4	.093	.100	2.35	2.55
c	.028	.039	0.70	1.00
D	.591	.630	15.00	16.00
D1	.472	.512	12.00	13.00
E	.394	.433	10.00	11.00
E1	.295	.335	7.50	8.50
e	.100	BASIC	2.55	BASIC
L	.512	.571	13.00	14.50
L1	.118	.138	3.00	3.50
T			42.5°	47.5°

Ref: IXYS CO 0177 R0

**ISOPLUS247 Outline**


SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.190	.205	4.83	5.21
A1	.090	.100	2.29	2.54
A2	.075	.085	1.91	2.16
b	.045	.055	1.14	1.40
b1	.075	.084	1.91	2.13
b2	.115	.123	2.92	3.12
C	.024	.031	0.61	0.80
D	.819	.840	20.80	21.34
E	.620	.635	15.75	16.13
e	.215	BSC	5.45	BSC
L	.780	.800	19.81	20.32
L1	.150	.170	3.81	4.32
Q	.220	.244	5.59	6.20
R	.170	.190	4.32	4.83
S	.520	.540	13.21	13.72
T	.620	.640	15.75	16.26
U	.065	.080	1.65	2.03

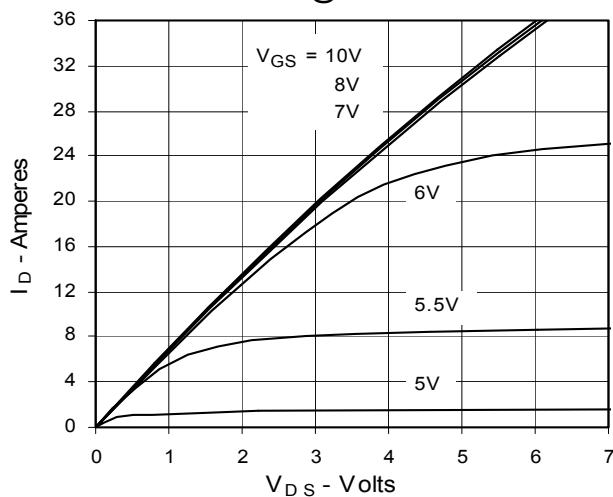
1 – GATE  
2 – DRAIN (COLLECTOR)  
3 – SOURCE (EMITTER)  
4 – NO CONNECTION

NOTE: This drawing will meet all dimensions requirement of JEDEC outline TO-247AD except screw hole.

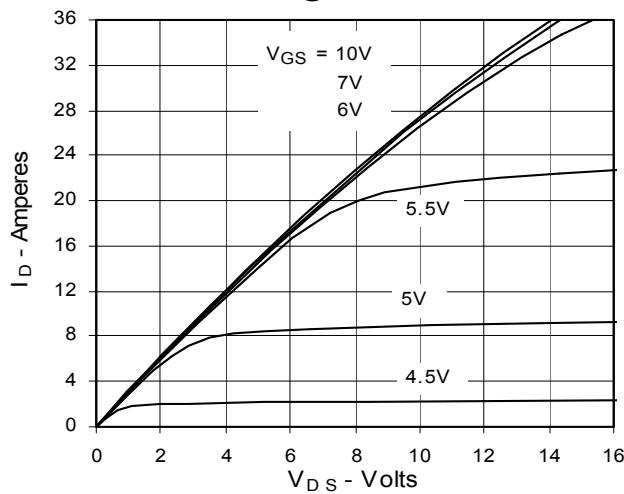
IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered by 4,835,592 4,931,844 5,049,961 5,237,481 6,162,665 6,404,065 B1 6,683,344 6,727,585 one or more of the following U.S. patents: 4,850,072 5,017,508 5,063,307 5,381,025 6,259,123 B1 6,534,343 6,710,405B2 6,759,692 4,881,106 5,034,796 5,187,117 5,486,715 6,306,728 B1 6,583,505 6,710,463 6,771,478 B2

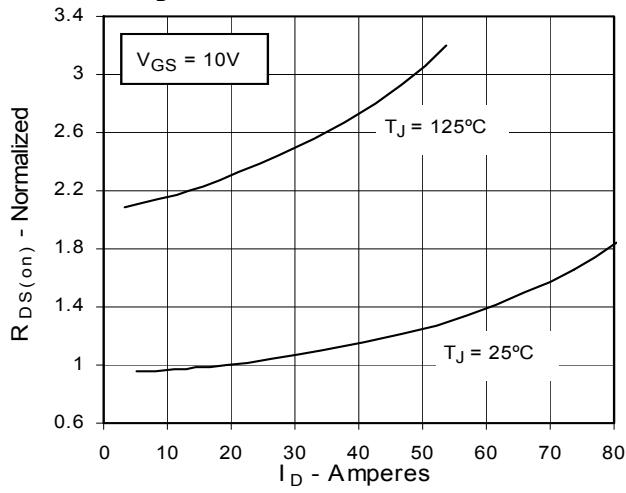
**Fig. 1. Output Characteristics  
@ 25°C**



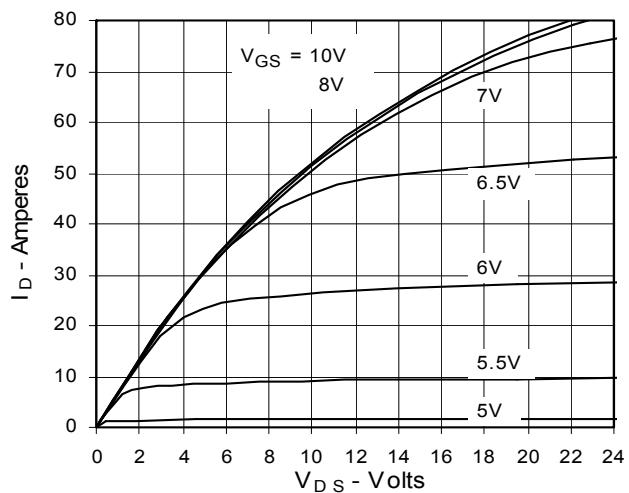
**Fig. 3. Output Characteristics  
@ 125°C**



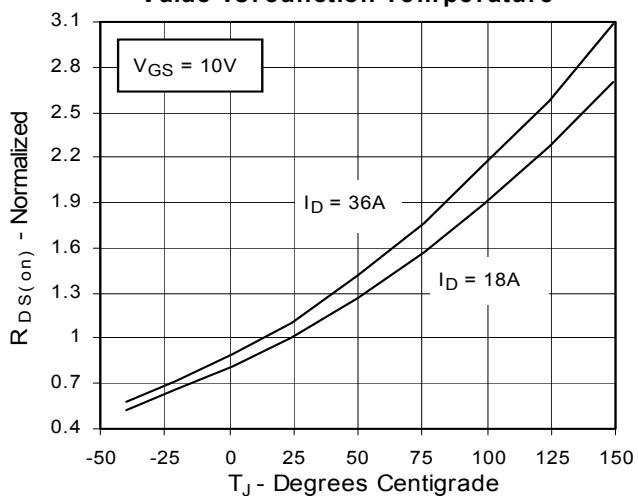
**Fig. 5.  $R_{DS(on)}$  Normalized to  
 $I_D = 18A$  Value vs. Drain Current**



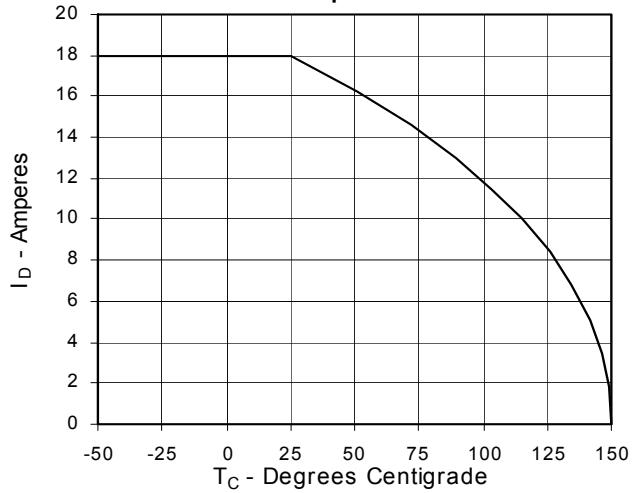
**Fig. 2. Extended Output Characteristics  
@ 25°C**

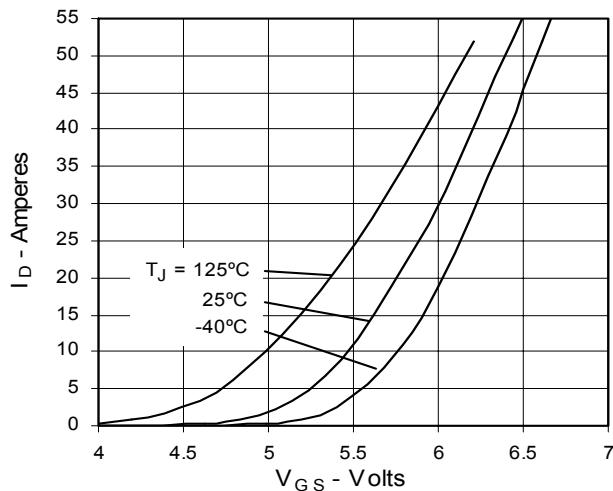
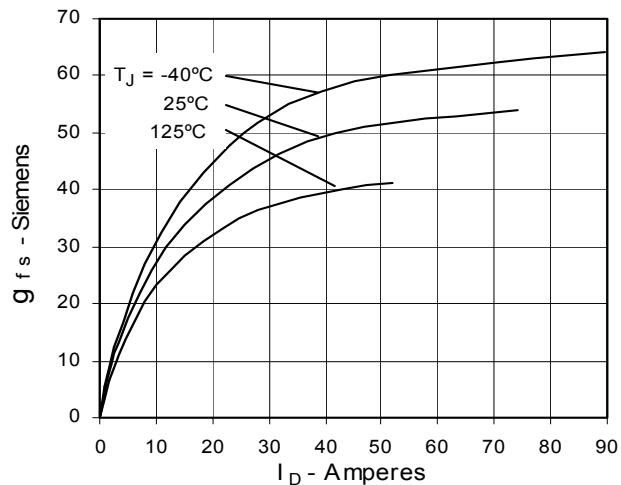
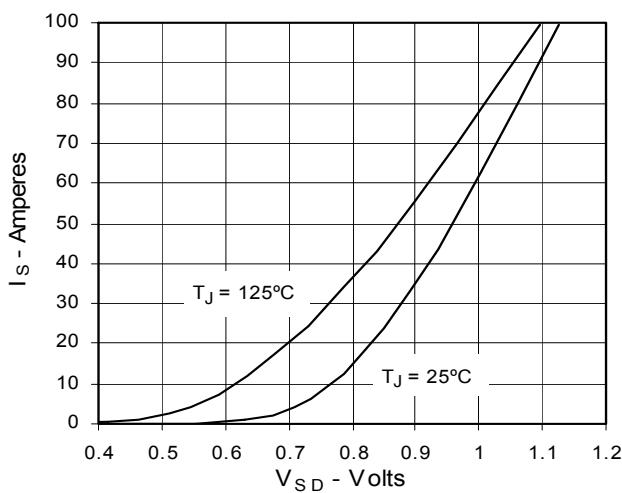
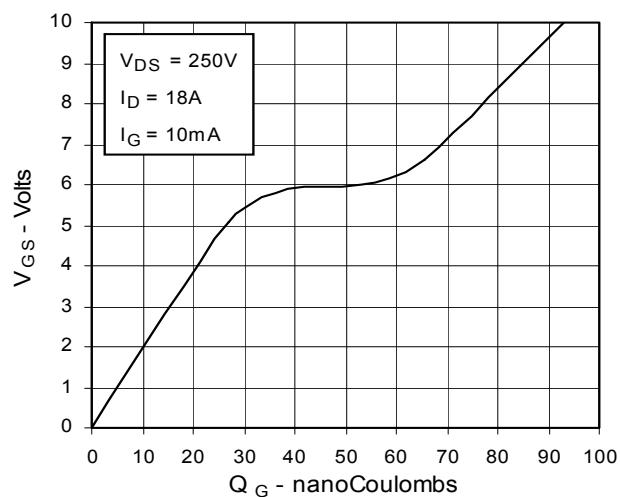
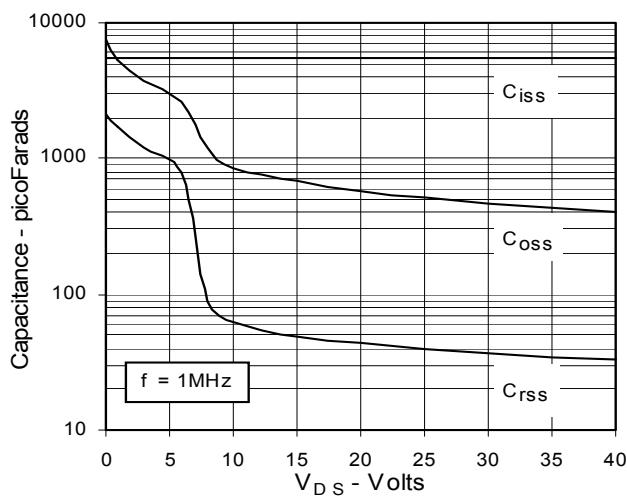
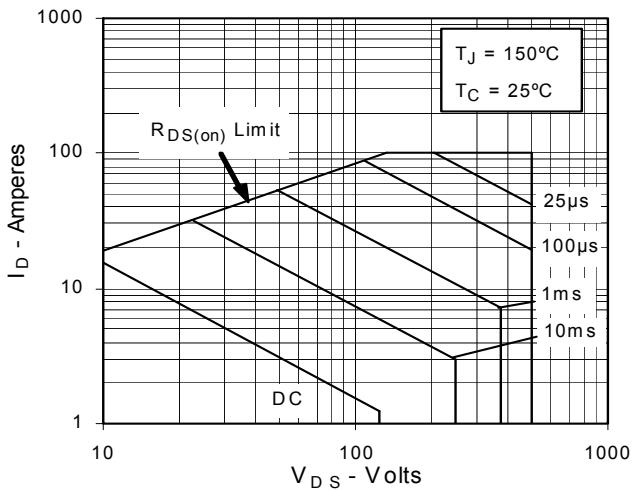


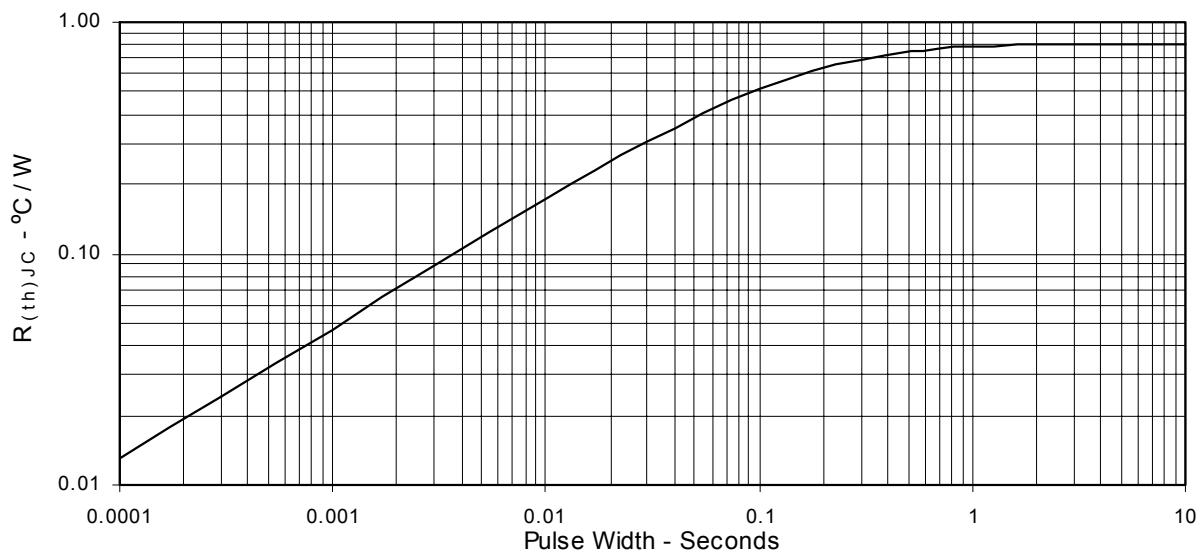
**Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 18A$   
Value vs. Junction Temperature**



**Fig. 6. Drain Current vs. Case  
Temperature**



**Fig. 7. Input Admittance**

**Fig. 8. Transconductance**

**Fig. 9. Source Current vs. Source-To-Drain Voltage**

**Fig. 10. Gate Charge**

**Fig. 11. Capacitance**

**Fig. 12. Forward-Bias Safe Operating Area**


**Fig. 13. Maximum Transient Thermal Resistance**



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