

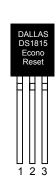
# DS1815 3.3V EconoReset with Push-Pull Output

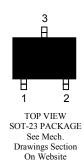
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#### **FEATURES**

- Automatically restarts a microprocessor after power failure
- Maintains reset for 150ms after V<sub>CC</sub> returns to an in-tolerance condition
- Reduces need for discrete components
- Precision temperature-compensated voltage reference and voltage sensor
- Accurate 5%, 10% or 20% power monitoring
- Low-cost TO-92 or space saving surfacemount SOT-23 packages available
- Push-pull output for low current operation
- Operating temperature -40°C to +85°C

### **PIN ASSIGNMENT**







BOTTOM VIEW TO-92 PACKAGE See Mech. Drawings Section On Website

#### PIN DESCRIPTION

## **TO-92**

1	RST	Active Low Reset Output
2	$V_{CC}$	Power Supply
3	GND	Ground

## **SOT-23**

1	RST	Active Low Reset Output
2	$V_{CC}$	Power Supply
3	GND	Ground

#### DESCRIPTION

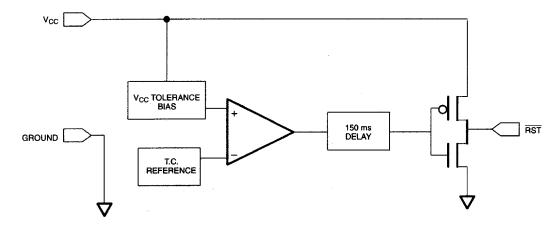
The DS1815 EconoReset uses a precision temperature reference and comparator circuit to monitor the status of the power supply  $(V_{CC})$ . When an out-of-tolerance condition is detected, an internal power-fail signal is generated which forces reset to the active state. When  $V_{CC}$  returns to an in-tolerance condition, the reset signal is kept in the active state for approximately 150ms to allow the power supply and processor to stabilize.

1 of 5 041002

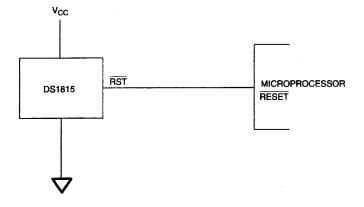
#### **OPERATION** — **POWER MONITOR**

The DS1815 provides the functions of detecting out-of-tolerance power supply conditions and warning a processor-based system of impending power failure. When  $V_{CC}$  is detected as out of tolerance, the  $\overline{RST}$  signal is asserted. On power-up,  $\overline{RST}$  is kept active for approximately 150ms after the power supply has reached the selected tolerance. This allows the power supply and microprocessor to stabilize before  $\overline{RST}$  is released.

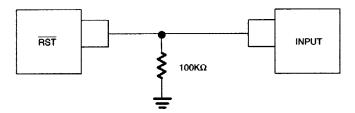
## **BLOCK DIAGRAM (CMOS OUTPUT)** Figure 1



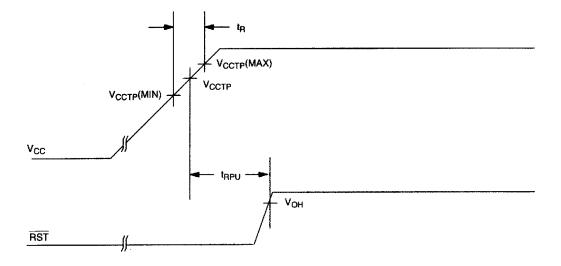
## **APPLICATION EXAMPLE** Figure 2



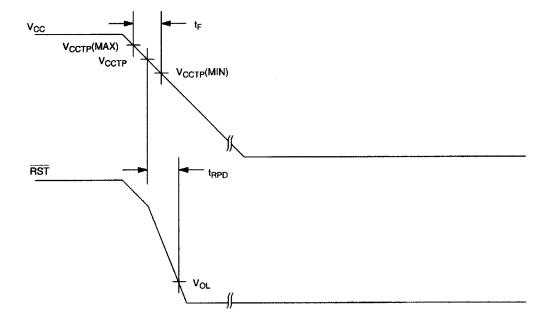
## APPLICATION DIAGRAM: RST VALID TO 0 VOLTS V<sub>CC</sub> ON THE DS1815 Figure 3



## TIMING DIAGRAM: POWER-UP Figure 4



## TIMING DIAGRAM: POWER-DOWN Figure 5



## **ABSOLUTE MAXIMUM RATINGS\***

Voltage on  $V_{CC}$  Pin Relative to Ground -0.5V to +7.0V

Voltage on RST Relative to Ground -0.5V to +5 $V_{CC}$  +0.5V

Operating Temperature -40°C to +85°C

Storage Temperature -55°C to +125°C

Soldering Temperature 260°C for 10 seconds

#### RECOMMENDED DC OPERATING CONDITIONS

(-40°C to +85°C)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
Supply Voltage	$V_{CC}$	0.0		5.5	V	1

## **DC ELECTRICAL CHARACTERISTICS** (-40°C to +85°C; $V_{CC}$ = 1.2V to 5.5V)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
Output Voltage @ -500μA	$V_{\mathrm{OH}}$	$V_{CC}$	$V_{CC}$		V	1
		-0.5V	-0.1V			
Output Current @ 2.4V	$I_{OH}$		350		μΑ	2
Output Current @ 0.4V	$I_{OL}$	+10			mA	2
Operating Current $V_{CC} < 5.5$	$I_{CC}$		28	35	μΑ	3
V <sub>CC</sub> Trip Point (DS1815-5)	$V_{CCTP}$	2.98	3.06	3.15	V	1
V <sub>CC</sub> Trip Point (DS1815-10)	$V_{CCTP}$	2.80	2.88	2.97	V	1
V <sub>CC</sub> Trip Point (DS1815-20)	$V_{CCTP}$	2.47	2.55	2.64	V	1
Output Capacitance	C <sub>OUT</sub>			10	pF	

## AC ELECTRICAL CHARACTERISTICS (-40°C to +85°C; $V_{CC}$ = 1.2V to 5.5V)

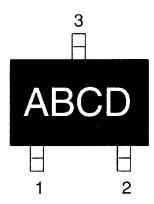
PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
TAKANIETEK	STNIDOL	141114	111	WIAA	UNITS	HOTES
RESET Active Time	$t_{RST}$	100	150	250	ms	4
V <sub>CC</sub> Detect to RST	$t_{RPD}$		2	5	μs	
V <sub>CC</sub> Slew Rate	$t_{ m F}$	300			μs	6
$(V_{CCTP} (MAX) \text{ to } V_{CCTP} (MIN))$						
V <sub>CC</sub> Slew Rate	$t_{R}$	0			ns	
$(V_{CCTP} (MIN) \text{ to } V_{CCTP} (MAX))$						
V <sub>CC</sub> Detect to RST	$t_{ m RPU}$	100	150	250	ms	4, 5

<sup>\*</sup> This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operation sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

## **NOTES:**

- 1. All voltages are referenced to ground.
- 2. Measured with  $V_{CC} \ge 2.7V$ .
- 3. Measured with  $\overline{RST}$  output open.
- 4. Measured with  $2.7V \le V_{CC} \le 3.3V$ .
- 5.  $t_R = 5 \mu s$ .
- 6. The  $t_F$  value is for reference in defining values for  $T_{RPD}$  and should not be considered a requirement for proper operation or use of the device.

### **PART MARKING CODES**



"A", "B", &"C" represent the device type.

810 . . . . DS1810

811 . . . . DS1811

812 . . . . DS1812

813 . . . . DS1813

815 . . . . DS1815

816 . . . . DS1816

817 . . . . DS1817

818 . . . . DS1818

"D" represents the device tolerance.

A . . . . . . 5%

B . . . . . 10%

C . . . . . 15%

D . . . . . . 20%