

Product Change Notification - SYST-30GGJH955

Date:	03 Apr 2017
Product Category:	
Affected CPNs:	
Notification subject:	Data Sheet - SCH3227/SCH3226/SCH3224/SCH3222 LPC IO with Reset Generation, HWM and Multiple S Data Sheet Document Revision
Notification text:	SYST-30GGJH955

Microchip has released a new DeviceDoc for the SCH3227/SCH3226/SCH3224/SCH3222 LPC IO with Reset Generation, HWM and Multiple S of devices. If you are using one of these devices please read the document located at [SCH3227/SCH3226/SCH3224/SCH3222 LPC IO with Reset Generation, HWM and Multiple S](#).

Notification Status: Final

Description of Change: 1) Updated diagrams in Figure 2-2, "SCH3226 Pin Diagram" and Figure 2-3, "SCH3224 Pin Diagram"
2) Added footnote to pin TEST, indicating that a connection to VSS is necessary for Table 2-2, "SCH3226 Summaries By Strap Option", Table 2-3, "SCH3224 Summary" and Table 2-4, "SCH3222 Summary"

Impacts to Data Sheet: None

Reason for Change: To Improve Manufacturability

Change Implementation Status: Complete

Date Document Changes Effective: 03 April 2017

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s): [SCH3227/SCH3226/SCH3224/SCH3222 LPC IO with Reset Generation, HWM and Multiple S](#)

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Affected Catalog Part Numbers (CPN)

SCH3227-SZ-TR

SCH3227-SZ

SCH3227I-SZ-TR

SCH3227I-SZ

SCH3226-SY

SCH3226I-SY-TR

SCH3226I-SY

SCH3222-SX-TR

SCH3222-SX

SCH3222I-SX-TR

SCH3222I-SX

SCH3224-SY-TR

SCH3224-SY

SCH3224I-SY-TR

SCH3224I-SY