

### **Features**

- Low Quiescent Current: 50µA
- Low Output Noise: 40µVRMS(10Hz~100kHz)
- Operating Voltage Range: 1.8V ~ 6.0V
- Low Dropout Voltage: 50mV@100mA
- Output Voltage:1.05~ 5.0VHigh Accuracy: ±2%(Typ.)
- · TTL-Logic-Controlled Shutdown Input
- · Excellent Line and Load Transient Response
- · Built-in Current Limiter, Short-Circuit Protection
- ESD Protected up to 4KV (HBM),200V(MM)
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- · Halogen Free Available Upon Request By Adding Suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)

# **Applications**

- Cellular and Smart Phones
- · Radio control systems
- · Laptop, Palmtops and PDAs
- · Digital Still and Video Cameras
- · MP3, MP4 Player
- · Battery-Powered Equipment

# **Description**

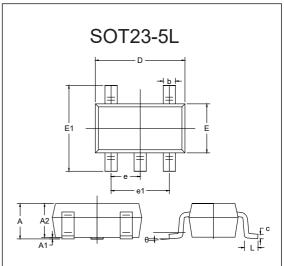
The MC6225 series are a group of positive voltage regulators manufactured by CMOS technologies with high ripple rejection, ultra-low noise, low power consumption and low dropout voltage, which can prolong battery life in portable electronics. The MC6225 series work with low-ESR ceramic capacitors, reducing the amount of board space necessary for power applications. The MC6225 series consume less than 0.1 $\mu$ A in shutdown mode and have fast turn-on time less than 50 $\mu$ S.The series are very suitable for the battery-powered equipments, such as RF applications and other systems requiring a quiet voltage source.

MCC Part Number	Device Marking
MC6225-1.2	ADdXX <sup>(1)</sup>
MC6225-1.8	ADJXX <sup>(1)</sup>
MC6225-2.5	ADqXX <sup>(1)</sup>
MC6225-3.0	ADvXX <sup>(1)</sup>
MC6225-3.3	ADyXX <sup>(1)</sup>

#### Note:

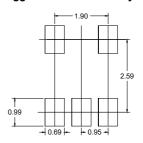
1. "XX" indicate DateCode.

# Low Noise CMOS Voltage Regulators



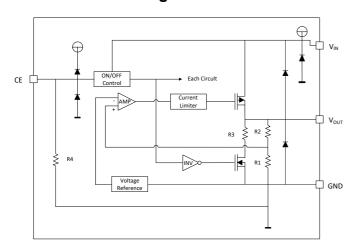
DIMENSIONS						
DIM	INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE	
Α	0.041	0.049	1.05	1.25		
A1	0.000	0.004	0.00	0.10		
A2	0.041	0.045	1.05	1.15		
b	0.012	0.020	0.30	0.50		
С	0.004	0.008	0.10	0.20		
D	0.111	0.119	2.82	3.02		
Е	0.059	0.067	1.50	1.70		
E1	0.104	0.116	2.65	2.95		
е	0.037(BSC)		0.950(BSC)			
e1	0.071	0.079	1.80	2.00		
L	0.012	0.024	0.30	0.60		
θ	0°	8°	0°	8°		

#### Suggested Solder Pad Layout

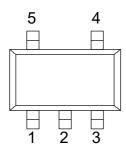




# **Functional Block Diagram**

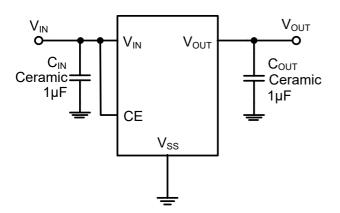


# Pin Configuration and Functions (Top View)



Number	Name	Function		
1	V <sub>IN</sub>	Power Input Pin		
2	V <sub>SS</sub>	Ground		
3	CE	Chip Enable Pin		
4	NC	No Connection		
5	V <sub>OUT</sub>	Output Pin		

# **Typical Application Circuit**





# **Absolute Maximum Ratings**

• Operating Free Air Temperature Range: -40~+85°C

• Operating Junction Temperature Range: -40~+125°C

• Storage Temperature Range: -40~+125°C

• Thermal Resistance: 258°C/W Junction to Ambient

Thermal Resistance: 82°C/W Junction to Case

Parameter	Symbol	Ratings	Units
Input Voltage	V <sub>IN</sub>	V <sub>SS</sub> -0.3 ~ V <sub>SS</sub> +7	V
Output Voltage	V <sub>OUT</sub>	V <sub>SS</sub> -0.3 ~ V <sub>IN</sub> +0.3	V
Output Current	I <sub>OUT</sub>	500	mA
Power Dissipation	P <sub>D</sub>	0.38	W

# Electrical Characteristics( $V_{IN}=V_{OUT}+1V$ , $C_{IN}=C_{OUT}=1\mu F$ , $T_A=25^{\circ}C$ , unless otherwise specified)

Paramete	er	Symbol	Conditions	Min.	Тур.	Max.	Units
Output Volta	age	V <sub>OUT(E)</sub> (2)	I <sub>OUT</sub> =1mA	Vout*0.98	Vout	Vout*1.02	V
Supply Curi	rent	Iss	I <sub>OUT</sub> =0		50	100	μA
Standby Cur	rent	Istby	CE = V <sub>SS</sub>		0.1	1	μA
Output Curi	rent	Іоит	_	500			mA
Dropout Volta	age	Vdif <sup>(3)</sup>	I <sub>OUT</sub> =100mA V <sub>OUT</sub> ≥3.3V		50		mV
Load Regula	ation	Δ <b>V</b> ουτ	V <sub>IN</sub> = V <sub>OUT</sub> +1V, 1mA≤I <sub>OUT</sub> ≤100mA		1		mV
Line Regula	tion		I <sub>OUT</sub> =10mA V <sub>OUT</sub> +1V≤V <sub>IN</sub> ≤6V		0.01	0.2	%/V
Output Volta Temperatu Characteris	ire		I <sub>ОUТ</sub> =10mA -40≤T≤+85		50		ppm
Short Curre	ent	Ishort	V <sub>OUT</sub> =V <sub>SS</sub>		50		mA
Input Volta	ge	Vin	_	1.8		6.0	V
Dower Supply	100Hz				75		
Power Supply	1kHz	PSRR	I <sub>OUT</sub> =50mA		80		dB
Rejection Rate	10kHz				80		
CE "High" Voltage		Vce"H"		1.5		V <sub>IN</sub>	V
CE "Low" Voltage		Vce"L"				0.3	V
C <sub>оит</sub> Auto-Disc Resistanc	•	RDISCHRG	$V_{IN}$ =5V, $V_{OUT}$ =3.0V, $V_{CE}$ = $V_{SS}$		60		Ω

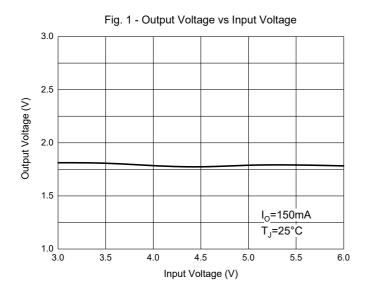
#### Note:

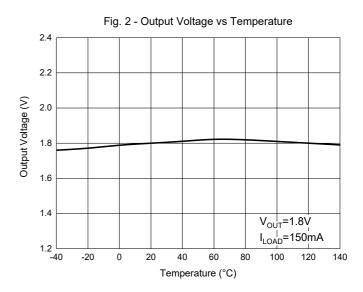
<sup>2.</sup>  $V_{OUT(E)}$ : Effective Output Voltage ( le. The output voltage when  $V_{IN}$  = ( $V_{OUT}$  +1.0V) and maintain a certain  $I_{OUT}$  Value).

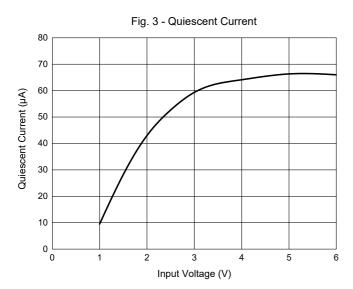
<sup>3.</sup> Vdif : The Difference Of Output Voltage And Input Voltage When Input Voltage Is Decreased Gradually Till Output Voltage Equals To 98% Of  $V_{\text{OUT}(E)}$ .

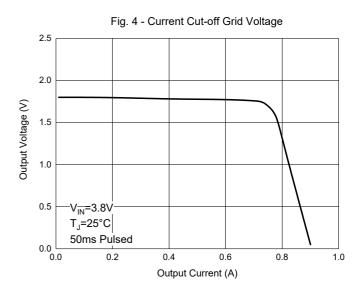


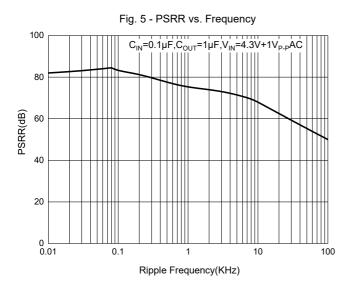
# **Curve Characteristics**













# **Ordering Information**

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

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