## **SS2003M**

# ON Semiconductor®

### Schottky Barrier Diode 30V, 2.0A, Low VF, Single MCPH6

http://onsemi.com

#### **Applications**

- · High frequency rectification (switching regulators, converters, choppers)
- · Halogen free compliance

#### **Features**

- · Small Switching noise
- Low forward voltage (IF=2A, VF max=0.40V)
- · Small package permitting applied sets to be small and slim

#### **Specifications**

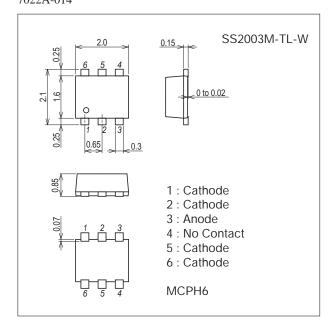
#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Repetitive Peak Reverse Voltage	VRRM		30	V
Nonrepetitive Peak Reverse Surge Voltage	VRSM		30	V
Average Output Current	IO		2.0	А
Surge Forward Current	I <sub>FSM</sub>	50Hz sine wave, 1 cycle	10	А
Junction Temperature	Tj		-55 to +125	°C
Storage Temperature	Tstg		-55 to +125	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### **Package Dimensions**

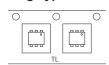
unit : mm (typ) 7022A-014



#### Ordering & Package Information

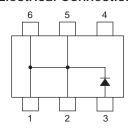
-	_		
Device	Package	Shipping	memo
SS2003M-TL-W	MCPH6 SC-88, SC-70-6, SOT-363	3,000 pcs./reel	Pb-Free and Halogen Free

#### Packing Type: TL



# Marking SF

#### **Electrical Connection**

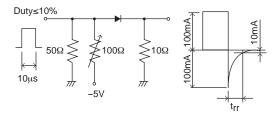


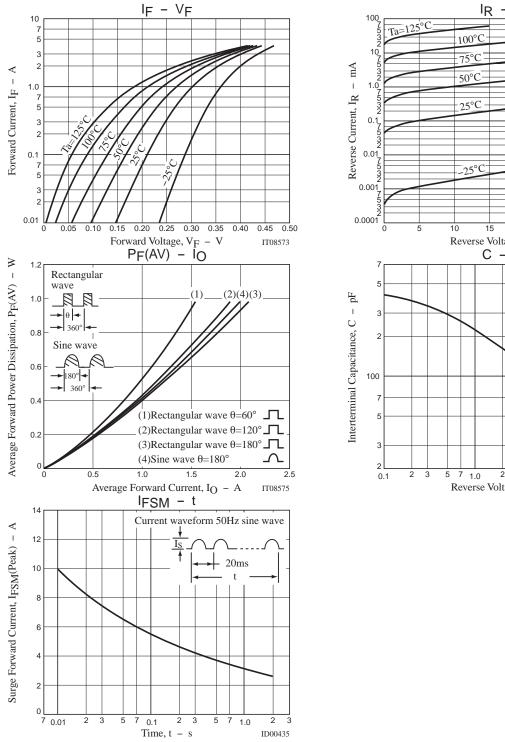
#### SS2003M

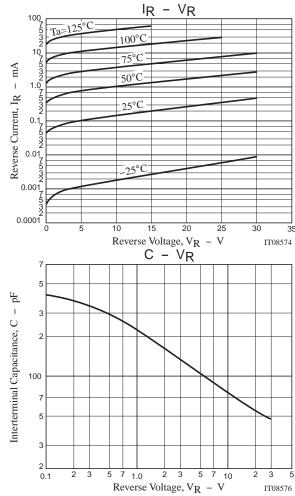
#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offit
Reverse Voltage	VR	I <sub>R</sub> =2.0mA	30			V
Forward Voltage	VF	IF=1.0A		0.30	0.35	V
		IF=2.0A		0.35	0.40	V
Reverse Current	IR	V <sub>R</sub> =15V			1.25	mA
Interterminal Capacitance	С	V <sub>R</sub> =10V, f=1MHz		75		pF
Reverse Recovery Time	t <sub>rr</sub>	IF=IR=100mA, See specified Test Circuit.			20	ns
Thermal Resistance	Rth(j-a)1	When mounted in Cu-foiled area of		93.4		°C/W
		1.44mm <sup>2</sup> ×0.03mm on glass epoxy substrate				
	Rth(j-a)2	When mounted on ceramic substrate (500mm <sup>2</sup> ×0.8mm)		71.4		°C / W

#### t<sub>rr</sub> Test Cicuit

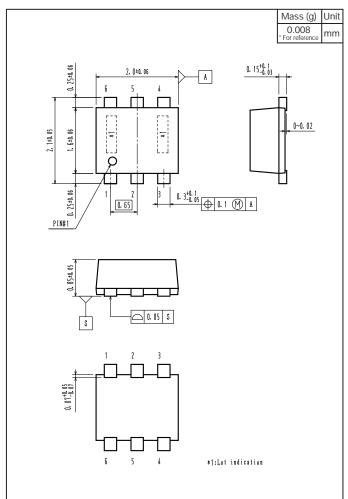




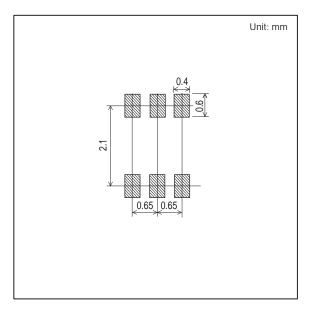


#### **Outline Drawing**

SS2003M-TL-W



#### **Land Pattern Example**



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