| PCN Number: | | 20170112000 | | | PCN Date: | Ja | January 17, 2017 | | | | |
|--|-----------------------------------|---------------|-------------------|----------------------|--------------|-----------------|---------------------|-------------------|------------------|-------|--|
| Title: Datasheet for LMV712-N/LMV712-N-Q1 | | | | | | | | | | | |
| Customer Contact: PCN Man | | | - | | | | Dej | pt: | Quality Services | | |
| Change | е Туре: | | | | | | | | | | |
| Assembly Site | | | | ☐ Design ☐ Waf | | | | | er Bump Site | | |
| Assembly Process | | | \boxtimes | Data Sheet | | | | Wafer | er Bump Material | | |
| Assembly Materials | | | | Part number change | | | | | fer Bump Process | | |
| Mechanical Specification | | | Щ | Test Site | | | | | er Fab Site | | |
| Packing/Shipping/Labeling | | | Ш | Test Process | | | | | er Fab Materials | | |
| | | | | Notification Details | | | Ш | Wafer Fab Process | | | |
| Description of Change: | | | | | | | | | | | |
| Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. LMV712-N, LMV712-N-Q1 | | | | | | | | | | | |
| SNOS534J – FEBRUARY 2001 – REVISED NOVEMBER 2016 www.ti.com | | | | | | | | | | com | |
| | | | | | | | | age | | | |
| Added ESD Ratings table, Feature Description section, Device Functional Modes, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section | | | | | | | | | | | |
| The datasheet number will be changing. | | | | | | | | | | | |
| Device Family | | | Change From: | | | | Change ⁻ | | | | |
| LMV712-N/LMV712-N-Q1 | | | | SNOS534I | | | | | SNOS534J | | |
| These changes may be reviewed at the datasheet links provided. http://www.ti.com/lit/ds/symlink/lmv712-n.pdf | | | | | | | | | | | |
| Reason | n for Change: | | | | | | | | | | |
| To accurately reflect device thermal characteristics. | | | | | | | | | | | |
| Anticip | ated impact o | n Fit, Form | , Fu | nction, Q | uality or Re | eliabi | ility | (posi | itive / negat | ive): | |
| | cipated impact. actual device. | This is a spe | cific | ation char | nge announc | emen | it oi | nly. Th | ere are no ch | anges | |
| Change | es to product i | identificatio | n re | esulting f | rom this PC | N: | | | | | |
| None. | | | | | | | | | | | |
| Product Affected: | | | | | | | | | | | |
| LMV71 | .2LD/NOPB | LMV | 712 | MM/NOPB | | LMV7 | 120 |)1MMX | (/NOPB | | |
| | .2LDX/NOPB | | | MMX/NOP | • | | | V712TL/NOPB | | | |
| · · | | | | | | | IV712TLX/NOPB | | | | |
| | ∠ 1 1111 | LITIV | // 12Q1MM/NOPD LM | | | _1'1 V / | IV/IZILA/NOFD | | | | |

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

| Location | E-Mail |
|--------------|--------------------------------|
| USA | PCNAmericasContact@list.ti.com |
| Europe | PCNEuropeContact@list.ti.com |
| Asia Pacific | PCNAsiaContact@list.ti.com |
| Japan | PCNJapanContact@list.ti.com |