



# Typical Applications

The HMC476SC70(E) is ideal for:

- Cellular / PCS / 3G
- WiBro / WiMAX / 4G
- Fixed Wireless & WLAN
- CATV, Cable Modem & DBS
- Microwave Radio & Test Equipment

#### Functional Diagram



# HMC476SC70 / 476SC70E

# SIGE HBT GAIN BLOCK MMIC AMPLIFIER, DC - 6 GHz

#### Features

P1dB Output Power: +12 dBm Gain: 20 dB Output IP3: +24 dBm Cascadable 50 Ohm I/Os Single Supply: +5V to +12V Industry Standard SC70 Package

### **General Description**

The HMC476SC70(E) is a SiGe Heterojunction Bipolar Transistor (HBT) Gain Block MMIC SMT amplifiers covering DC to 6 GHz. This industry standard SC70 packaged amplifier can be used as a cascadable 50 Ohm RF/IF gain stage as well as a LO or PA driver with up to +12 dBm output power. The HMC476SC70(E) offers 20 dB of gain with a +24 dBm output IP3 at 850 MHz while requiring only 35 mA from a single positive supply. The Darlington topology results in reduced sensitivity to normal process variations and excellent gain stability over temperature while requiring a minimal number of external bias components.

# Electrical Specifications, Vs= 5V, Rbias= 56 Ohm, $T_A$ = +25° C

Parameter		Min.	Тур.	Max.	Units
Gain	DC - 2.0 GHz 2.0 - 4.0 GHz 4.0 - 6.0 GHz	16 13 9	19 16 12		dB dB dB
Gain Variation Over Temperature	DC - 6 GHz		0.008	0.012	dB/ °C
Input Return Loss	DC - 4 GHz 4.0 - 6.0 GHz		20 15		dB dB
Output Return Loss	DC - 4 GHz 4.0 - 6.0 GHz		20 13		dB dB
Reverse Isolation	DC - 6 GHz		18		dB
Output Power for 1 dB Compression (P1dB)	0.5 - 4.0 GHz 4.0 - 6.0 GHz	9.0 8.0	12.0 11.0		dBm dBm
Output Third Order Intercept (IP3) (Pout= 0 dBm per tone, 1 MHz spacing)	0.5 - 4.0 GHz 4.0 - 6.0 GHz		24 22		dBm dBm
Noise Figure	0.5 - 4.0 GHz 4.0 - 6.0 GHz		2.5 3.0		dB dB
Supply Current (Icq)			35	42	mA

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#### Broadband Gain & Return Loss



Input Return Loss vs. Temperature



**Reverse Isolation vs. Temperature** 



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#### Gain vs. Temperature



#### Output Return Loss vs. Temperature



# Noise Figure vs. Temperature



AMPLIFIERS - DRIVER & GAIN BLOCK - SMT

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#### P1dB vs. Temperature



**Output IP3 vs. Temperature** 



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Psat vs. Temperature



Gain, Power & OIP3 vs. Supply Voltage for Constant Icc = 35 mA @ 850 MHz







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Collector Bias Voltage (Vcc)

Collector Bias Current (Icc)

**Junction Temperature** Continuous Pdiss (T = 85 °C)

**Thermal Resistance** 

**Operating Temperature** ESD Sensitivity (HBM)

(junction to lead) Storage Temperature

RF Input Power (RFIN)(Vcc = +2.4V)

(derate 7.75 mW/°C above 85 °C)

Absolute Maximum Ratings

+6V

45 mA

+5 dBm 150 °C

0.504 W

129 °C/W

Class 1A

-65 to +150 °C -40 to +85 °C



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ELECTROSTATIC SENSITIVE DEVICE **OBSERVE HANDLING PRECAUTIONS** 

# **Outline Drawing**



- 1. PACKAGE BODY MATERIAL: LOW STRESS INJECTION MOLDED PLASTIC SILICA AND SILICON IMPREGNATED.
- DIMENSIONS ARE IN INCHES [MILLIMETERS].
- DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
- A DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.
- 7. ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND.

#### Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking
HMC476SC70	Low Stress Injection Molded Plastic	Sn/Pb	MSL1 <sup>[1]</sup>	476E
HMC476SC70E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 <sup>[2]</sup>	476E

[1] Max peak reflow temperature of 235 °C [2] Max peak reflow temperature of 260 °C

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# **Pin Descriptions**

Pin Number	Function	Description	Interface Schematic
1, 2, 4, 5	GND	These pins must be connected to RF/DC ground.	
3	RFIN	This pin is DC coupled. An off chip DC blocking capacitor is required.	RFOUT
6	RFOUT	RF output and DC Bias (Vcc) for the output stage.	

# **Application Circuit**



# Recommended Bias Resistor Values for Icc= 35 mA, Rbias= (Vs - Vcc) / Icc

Supply Voltage (Vs)	5V	8V	10V	12V
RBIAS VALUE	56 Ω	130 Ω	<b>180</b> Ω	240 Ω
RBIAS POWER RATING	1/8 W	1/4 W	1/4 W	1/2 W

Note:

- 1. External blocking capacitors are required on RFIN and RFOUT.
- 2. RBIAS provides DC bias stability over temperature.

# **Recommended Component Values for Key Application Frequencies**

Compone	ant				Frequen	cy (MHz)			
Compon	5111	50	900	1900	2200	2400	3500	5200	5800
L1		270 nH	56 nH	18 nH	18 nH	15 nH	8.2 nH	6.8 nH	3.3 nH
C1, C2		0.01 µF	100 pF	100 pF	100 pF	100 pF	100 pF	100 pF	100 pF

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# **Evaluation PCB**



# List of Materials for Evaluation PCB 118038 [1]

Item	Description
J1 - J2	PCB Mount SMA Connector
J3 - J4	DC Pin
C1 - C3	100 pF Capacitor, 0402 Pkg.
C4	1000 pF Capacitor, 0603 Pkg.
C5	2.2 µF Capacitor, Tantalum
R1	50 Ohm Resistor, 1210 Pkg.
L1	18 nH Inductor, 0603 Pkg.
U1	HMC476SC70(E)
PCB <sup>[2]</sup>	117360 Evaluation PCB

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.

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