

A Product Line of Diodes Incorporated



4-Bit Bus Switch, Enable Low 1.8V/2.5V/3.3V, High-Bandwidth, Hot Plug

Features

- → Near-Zero propagation delay
- → 5-ohm switches connect inputs to outputs
- → High signal passing bandwidth (500 MHz)
- → Beyond Rail-to-Rail switching:
 - 0 to 5V switching with 3.3V power supply
- 0 to 3.3V switching with 2.5V power supply
- → 5V I/O tolerant with supply in OFF and ON state
- → 1.8V, 2.5V and 3.3V supply voltage operation
- ➔ Hot Insertion Capable
- → Industrial Operating Temperature: -40°C to +85°C
- → 8kV ESD Protection (human body model)
- → Latch-up Performance: >200mA per JESD17
- → Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- → Halogen and Antimony Free. "Green" Device (Note 3)
- ➔ For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

→ Packaging (Pb-free & Green available):
-14-pin TSSOP (L)
-14-pin QFN (ZB)

Pin Configuration



Description

The PI3CH400 is a low voltage, 4-channel switch designed with fast individual enables. The switch introduces no additional ground bounce noise or additional propagation delay.

The PI3CH400 device has active low enables. It is very useful in switching signals that have high bandwidth (500 MHz).

Applications

- → High Bandwidth Data Switching
- → Hot-Docking
- ➔ Analog Signal Switching
- ➔ Differential Signal Switching

Pin Description

Pin No	Pin Name	Description
1, 4, 10	$\overline{\mathrm{EN}}_{0},\overline{\mathrm{EN}}_{1},\overline{\mathrm{EN}}_{2}$	Switch Enables
2, 5, 9, 12	A0-A3	A Ports
3, 6, 8, 11	B0-B3	B Ports
14	V _{CC}	Power
7	GND	Ground

Block Diagram



Truth Table⁽¹⁾

ĒN _x	Ax	Bx	V _{CC}	Function
X ⁽²⁾	Hi-Z	Hi-Z	GND	Disconnect
Н	Hi-Z	Hi-Z	V _{CC}	Disconnect
L	Bx	Ax	V _{CC}	Connect
Note:				

1. H=High Voltage Level; L=Low Voltage Level; Hi-Z=High Impedance

2. A pull-up resistor should be provided for power-up protection

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Note



Maximum Ratings

Storage Temperature	-65°C to +150°C
Ambient Temperature with Power Applied	-40° C to $+85^{\circ}$ C
Supply Voltage to Ground Potential	-0.5V to +4.6V
DC Input Voltage	-0.5V to + 6.0V
DC Output Current	
Power Dissipation	

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics

3.3V supply (Over operating range, $T_A = -40 \sim +85$ °C, $V_{CC} = 3.3V \pm 10\%$, unless otherwise noted)

Symbol	Description	Test Conditions ⁽¹⁾	Min	Typ ⁽²⁾	Max	Unit
V _{IH}	Control Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0	-	-	V
V _{IL}	Control Input LOW Voltage	Guaranteed Logic LOW Level	-0.5	-	0.8	V
V _{IK}	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18mA$	-	-1.3	-1.8	V
I _{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$	-	-	±1	μΑ
I _{IL}	Input Low Current	$V_{CC} = Max., V_{IN} = GND$	-	-	± 1	μΑ
I _{OZH}	High-Impedance Current	$0 \le A, B \le V_{CC}$	-	-	±1	μΑ
D	Switch On-Resistance ⁽³⁾	$V_{CC} = Min., V_{IN} = 0.0V$ $I_{ON} = -48mA$ or -64mA	-	4	6	Ω
R _{ON} Sw	Switch On-Resistance	$V_{CC} = Min., V_{IN} = 3.6V$ $I_{ON} = -15mA$	-	5	8	52

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at $V_{CC} = 3.3V$, $T_A = 25$ °C ambient and maximum loading.

3. Measured by the voltage drop between A and B pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (Ax, Bx) pins.

2.5V supply (Over o	noroting rongs T	- 40 + 859C V	-2.5 V + 100	unloss otherwise noted)
2.5 V supply (Over 0	perating range, r _A	$= -40 \approx +65 \text{ C}, \text{ v}$	$CC-2.3 v \pm 10\%$,	unless otherwise noted)

Symbol	Description	Test Conditions ⁽¹⁾	Min	Typ ⁽²⁾	Max	Unit
V _{IH}	Control Input HIGH Voltage	Guaranteed Logic HIGH Level	1.8	-	V _{CC} +0.3	V
V _{IL}	Control Input LOW Voltage	Guaranteed Logic LOW Level	-0.3	-	0.8	V
V _{IK}	Clamp Diode Voltage	$V_{CC} = Max., I_{IN} = -6mA$	-	-0.7	-1.8	V
I _{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$	-	-	±1	μΑ
I _{IL}	Input Low Current	$V_{CC} = Max., V_{IN} = GND$	-	-	±1	μΑ
I _{OZH}	High-Impedance Current	$0 \le A, B \le V_{CC}$	-	-	±1	μΑ
D	Switch On-Resistance ⁽³⁾	$V_{CC} = Min., V_{IN} = 0.0V$ $I_{ON} = -48mA$	-	4	8	Ω
R _{ON}	Switch On-Resistance	$V_{CC} = Min., V_{IN} = 2.25V$ $I_{ON} = -15mA$	-	7	14	52

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at $V_{CC} = 2.5V$, $T_A = 25$ °C ambient and maximum loading.

3. Measured by the voltage drop between A and B pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (Ax, Bx) pins.



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PI3CH400

Symbol	Description	Test Conditions ⁽¹⁾	Min	Typ ⁽²⁾	Max	Unit
V _{IH}	Control Input HIGH Voltage	Guaranteed Logic HIGH Level	1.2	-	V _{CC} +0.3	V
V _{IL}	Control Input LOW Voltage	Guaranteed Logic LOW Level	-0.3	-	0.6	V
V _{IK}	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18mA$	-	-0.7	-1.8	V
I _{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$	-	-	±1	μΑ
I _{IL}	Input Low Current	$V_{CC} = Max., V_{IN} = GND$	-	-	±1	μΑ
I _{OZH}	High-Impedance Current	$0 \le A, B \le V_{CC}$	-	-	±1	μΑ
R _{ON}	Switch On-Resistance ⁽³⁾	$V_{CC} = Min., V_{IN} = 0.0V$ $I_{ON} = -48mA$	-	4	8	Ω
INON	Switch On-Resistance	$V_{CC} = Min., V_{IN} = 1.6V$ $I_{ON} = -15mA$	-	10	25	52

1.8V supply (Over operating range, $T_A = -40 \sim +85$ °C, $V_{CC} = 1.8V \pm 10\%$, unless otherwise noted)

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at $V_{CC} = 1.8V$, $T_A = 25$ °C ambient and maximum loading.

3. Measured by the voltage drop between A and B pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (Ax, Bx) pins.

Capacitance ($T_A = 25$ °C, f=1MHz)

Symbol ⁽¹⁾	Description	Test Conditions	Typ ⁽²⁾	Unit
C _{IN}	Input Capacitance		2.0	
C _{OFF}	A/B Capacitance, Switch Off	$V_{IN} = 0V$	3.5	pF
C _{ON}	A/B Capacitance, Switch On		7.0	

Note:

1. These parameters are determined by device characterization but are not production tested

Power Supply Characteristics

Symbol	Description	Test Conditions ⁽¹⁾	Min	Тур	Max	Unit
I _{CC}	Quiescent Power Supply Current	$V_{CC} = 3.6V, V_{IN} = GND \text{ or } V_{CC}$	-	0.2	0.5	mA

Note:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.

2. Typical values are at +25°C ambient

Dynamic Electrical Characteristics

(Over Operating Range, $T_A = -40 \sim +85$ °C, $V_{CC} = 3.3V \pm 10\%$)

Symbol	Description	Test Conditions	Min	Тур	Max	Unit
X _{TALK}	Crosstalk	10MHz	-	-60	-	dB
O _{IRR}	Off-Isolation	10MHz	-	-60	-	uБ
BW	-3dB Bandwidth	See test Diagram	-	500	-	MHz





Switch Characteristics

Symbol	Description	Test Conditions ⁽¹⁾	Min	Тур	Max	Unit
$t_{PLH,} t_{PHL}$	Propagation Delay ^(2, 3) Ax to Bx, Bx to Ax	See test Diagram	-	-	0.3	
$t_{PZH,} t_{PZL}$	Enable Time $\overline{EN}x$ to Ax or Bx	See test Diagram	1.5	-	9.0	ns
$t_{PHZ,} t_{PLZ}$	Disable Time $\overline{EN}x$ to Ax or Bx	See test Diagram	1.5	-	9.0	

Note:

1. See test circuit and waveforms.

2. This parameter is guaranteed but not tested on Propagation Delays.

3. The switch contributes no propagation delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.30ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Over 2.5V Operating Range

Symbol	Description	Test Conditions ⁽¹⁾	Min	Тур	Max	Unit
t _{PLH,} t _{PHL}	Propagation Delay ^(2, 3) Ax to Bx, Bx to Ax	See test Diagram	-	-	0.3	
$t_{PZH,} t_{PZL}$	Enable Time $\overline{EN}x$ to Ax or Bx	See test Diagram	1.5	-	15.0	ns
$t_{PHZ,} t_{PLZ}$	Disable Time $\overline{EN}x$ to Ax or Bx	See test Diagram	1.5	-	12.0	

Note:

1. See test circuit and waveforms.

2. This parameter is guaranteed but not tested on Propagation Delays.

3. The switch contributes no propagation delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.30ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Over 1.8V Operating Range

Symbol	Description	Test Conditions ⁽¹⁾	Min	Тур	Max	Unit
t _{PLH,} t _{PHL}	Propagation Delay ^(2, 3) Ax to Bx, Bx to Ax	See test Diagram	-	-	0.3	
$t_{PZH,} t_{PZL}$	Enable Time $\overline{EN}x$ to Ax or Bx	See test Diagram	1.5	-	25.0	ns
$t_{PHZ,} t_{PLZ}$	Disable Time $\overline{EN}x$ to Ax or Bx	See test Diagram	1.5	-	12.0	

Note:

1. See test circuit and waveforms.

2. This parameter is guaranteed but not tested on Propagation Delays.

3. The switch contributes no propagation delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.30ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.





Test Circuit for Electrical Characteristics



Notes:

- 1. C_L = Load capacitance: includes jig and probe capacitance.
- 2. RT = Termination resistance: should be equal to ZOUT of the Pulse Generator
- 3. All input impulses are supplied by generators having the following characteristics: PRR \leq 10 MHz, Z_O = 50-ohm, t_R \leq 2.5ns, t_F \leq 2.5ns.
- 4. The outputs are measured one at a time with one transition per measurement.

Switch Positions

Test	Switch
t _{PLZ} , t _{PZL}	6.0V .
t _{PHZ} , t _{PZH} .	GND
Prop Delay	Open

Test Circuit for Dynamic Electrical Characteristics







Switching Waveforms



Applications Information

Logic Inputs

The logic control inputs can be driven up to 3.6V regardless of the supply voltage. For example, given a +3.3V supply, $\overline{\text{EN}}$ maybe driven LOW to 0V and HIGH to 3.6V. Driving $\overline{\text{EN}}$ Rail-to-Rail[®] minimizes power consumption.

Hot Insertion

For Datacom and Telecom applications that have ten or more volts passing through the backplane, a high voltage from the power supply may be seen at the device input pins during hot insertion. The PI3CH400 devices have maximum limits of 6V and 120mA for 20ns. If the power is higher or applied for a longer time or repeatedly reaches the maximum limits, the devices can be damaged.

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.

Part Marking

ZB Package



YYWW: Date Code (Year & Workweek) J: Assembly Site Code G: Wafer Fab Site Code L Package



Z: Fixed Code Y: Date Code (Year) W: Date Code (Workweek) 1st X: Assembly Site Code 2nd X: Fab Site Code Bar above "I" means Fab3 of MGN Bar above fab code means Cu wire





Packaging Mechanical

14-TSSOP (L)



16-0060





14-Pin QFN (ZB)



21-1377

For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

Ordering Information

Part Number	Package Cede	Package Description			
PI3CH400LEX	L	14-Pin, 173mil Wide (TSSOP)			
PI3CH400ZBEX	ZB	14-Pin, 3.5x3.5 (TQFN)			
PI3CH400ZBEX-13R	ZB	14-Pin, 3.5x3.5 (TQFN), Pin 1 orientation is Top			
Notor					

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. E = Pb-free and Green

5. X suffix = Tape/Reel

6. For packaging detail, go to our website at: https://www.diodes.com/assets/MediaList-Attachments/Diodes-Package-Information.pdf





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