

## PCN / EOL Notification

Product Change Notification Number: PDF030				Notification Date: June 29, 2017				
Title: Addition of UMC 8S Wafer Fabrication Facility for All AT25DF321A Products								
Product Identification: All wafer and package options of the Industrial Temperature grade (-40°C to +85°C) AT25DF321A Reason for Change:								
Material / Composition			Design / Firmware		Manufacturing Location		on 🗌	
Processing / Manufacturi	ing 🖂		Logistics		Quality/Reliability			
Change Description:	hange Description:							
This product is currently manufactured at UMC Fab 8D (Taiwan). Adesto will add UMC Fab 8S (Taiwan) as a wafer fabrication facility for all AT25DF321A family of devices as shown in Table 1. The UMC Fab 8S facility will manufacture the parts to the same datasheet specification as the current UMC Fab 8D. No Datasheet changes are being implemented as a result of this manufacturing location update. UMC Fab 8S parts will be compatible in form, fit, and function to the UMC Fab 8D.								
Table 1		<u> </u>						
Ordering Code <sup>(1)</sup>	Package	L	ead (Pad) Finish	Operating Voltag	e Max. Fr	eq. (MHz)	Operation Range	
AT25DF321A-MH-Y AT25DF321A-MH-T	8MA1		NiPdAu				Industrial	
AT25DF321A-SH-B AT25DF321A-SH-T	882			2.7V to 3.6V	1	.00	(-40°C to +85°C)	
AT25DF321A-CCU-T	9CC1		SnAgCu					
Qualification Data:	A	vailabl	e 🗌		/ill be available 🛛 🕅 nm/dd/yr): 09/15/17		Not Applicable	
Samples:	A	vailabl	e 🗌	Will be availab			Not Applicable	
Quantifiable Impact on Q None	uality& R	eliabilit	ty:					
Forecasted Availability Date : Oct. 1, 2017 Last Time Buy Date : N/A Last Ship Date : N/A								
Adesto Contact: pcn@adestotech.com								

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Company:
Name:
Title:
Date:
Email Address:
Location:
Comments:

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