

# **MOSFET** - Power, N-Channel, SUPERFET® III, **FAST**

# 650 V, 67 mΩ, 40 A

# NTH4L067N65S3H

#### Description

SUPERFET III MOSFET is onsemi's brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This advanced technology is tailored to minimize conduction loss, provides superior switching performance, and withstand extreme dv/dt rate.

Consequently, SUPERFET III MOSFET FAST series is very suitable for the various power systems for miniaturization and higher efficiency.

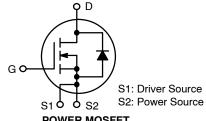
#### **Features**

- $700 \text{ V} @ \text{T}_{\text{J}} = 150^{\circ}\text{C}$
- Typ.  $R_{DS(on)} = 55 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q<sub>g</sub> = 80 nC)
- Low Effective Output Capacitance (Typ. C<sub>oss(eff.)</sub>= 691 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

#### **Applications**

- Telecom / Server Power Supplies
- Industrial Power Supplies
- UPS / Solar

V <sub>DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
650 V	67 mΩ @ 10 V	40 A

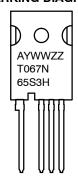


**POWER MOSFET** 



TO-247-4LD CASE 340CJ

#### **MARKING DIAGRAM**



Α YWW ZZ T067N65S3H = Assembly Site Code = Data Code (Year & Week)

= Assembly Lot Code = Specific Device Code

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

## **ABSOLUTE MAXIMUM RATINGS** ( $T_C = 25^{\circ}C$ , Unless otherwise noted)

Symbol	Parameter		Value	Unit	
$V_{DSS}$	Drain to Source Voltage	n to Source Voltage		V	
$V_{GSS}$	Gate to Source Voltage - DC		±30	V	
		- AC (f > 1 Hz)	±30		
I <sub>D</sub>	Drain Current	Drain Current – Continuous (T <sub>C</sub> = 25°C)		Α	
		– Continuous (T <sub>C</sub> = 100°C)	25		
I <sub>DM</sub>	Drain Current	- Pulsed (Note 1)	112	Α	
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)	422	mJ		
I <sub>AS</sub>	Avalanche Current (Note 2)		6.5	Α	
E <sub>AR</sub>	Repetitive Avalanche Energy (Note 1)	2.66	mJ		
dv/dt	MOSFET dv/dt		100	V/ns	
	Peak Diode Recovery dv/dt (Note 3)	20			
$P_{D}$	Power Dissipation	(T <sub>C</sub> = 25°C)	266	W	
		- Derate Above 25°C	2.13	W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C	
TL	Maximum Lead Temperature for Soldering, 1/8"	260	°C		

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Repetitive rating: pulse width limited by maximum junction temperature. 
  2.  $I_{AS}=6.5$  A,  $R_{G}=25$   $\Omega$ , starting  $T_{J}=25^{\circ}C$ . 
  3.  $I_{SD}\leq 20$  A, di/dt  $\leq 200$  A/ $\mu$ s,  $V_{DD}\leq 400$  V, starting  $T_{J}=25^{\circ}C$ .

#### THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	0.47	°C/W
$R_{ heta JA}$	Thermal Resistance, Junction to Ambient, Max.	40	

## PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
NTH4L067N65S3H	T067N65S3H	TO-247 A04	Tube	N/A	N/A	30 Units

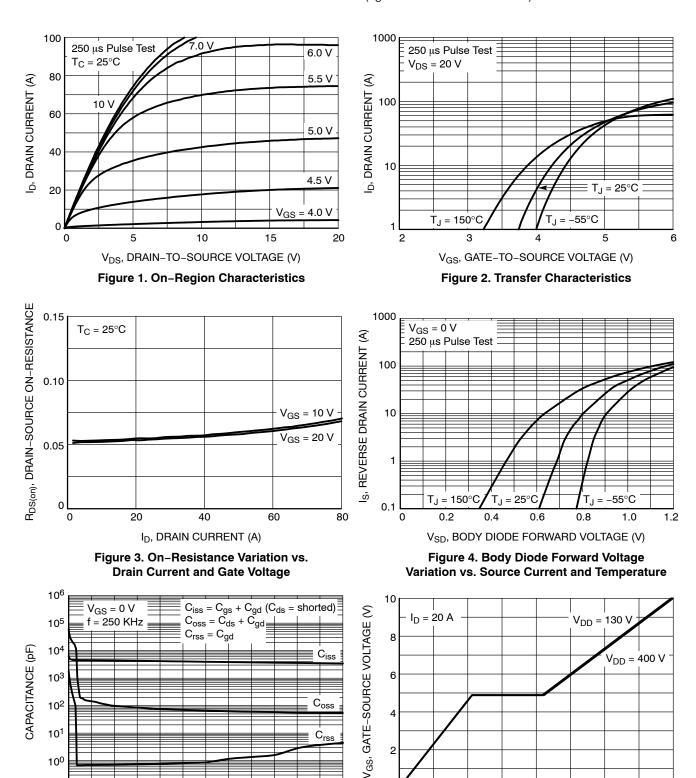
# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	ERISTICS			-	-	
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}, T_J = 25^{\circ}\text{C}$	650	_	_	V
		V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA, T <sub>J</sub> = 150°C	700	_	_	V
$\Delta BV_{DSS}/\Delta T_{J}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 10 mA, Referenced to 25°C	-	0.63	-	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V	-	<b>1</b> –	2	μΑ
		V <sub>DS</sub> = 520 V, T <sub>C</sub> = 125°C	-	1.6	_	
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V	_	_	±100	nA
ON CHARACTE	RISTICS			•		
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}$ , $I_D = 3.9 \text{ mA}$	2.4	_	4.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A	-	55	67	mΩ
9FS	Forward Transconductance	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 20 A	_	28	_	S
DYNAMIC CHAI	RACTERISTICS			•		
C <sub>iss</sub>	Input Capacitance			3750	_	pF
C <sub>oss</sub>	Output Capacitance	$V_{DS} = 400 \text{ V}, V_{GS} = 0 \text{ V}, f = 250 \text{ kHz}$	_	60	_	pF
C <sub>oss(eff.)</sub>	Effective Output Capacitance	V <sub>DS</sub> = 0 V to 400 V, V <sub>GS</sub> = 0 V	-	691	_	pF
C <sub>oss(er.)</sub>	Energy Related Output Capacitance	V <sub>DS</sub> = 0 V to 400 V, V <sub>GS</sub> = 0 V		107	_	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10 V		-	80	_	nC
$Q_{gs}$	Gate to Source Gate Charge	V <sub>DS</sub> = 400 V, I <sub>D</sub> = 20 A, V <sub>GS</sub> = 10 V (Note 4)	_	21	_	nC
$Q_{gd}$	Gate to Drain "Miller" Charge	(1333-1)	-	20	_	nC
ESR	Equivalent Series Resistance	eries Resistance f = 1 MHz		0.6	_	Ω
SWITCHING CH	IARACTERISTICS					
t <sub>d(on)</sub>	Turn-On Delay Time		-	28	_	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{DD} = 400 \text{ V}, I_D = 20 \text{ A},$	-	7.2	_	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{GS} = 10 \text{ V, } R_g = 4.7 \Omega$ (Note 4)	-	81	_	ns
t <sub>f</sub>	Turn-Off Fall Time		-	2.6	_	ns
SOURCE-DRAII	N DIODE CHARACTERISTICS					
I <sub>S</sub>	Maximum Continuous Source to Drain Diode Forward Current		-	_	40	Α
I <sub>SM</sub>	Maximum Pulsed Source to Drain Diode Forward Current		-	_	112	Α
V <sub>SD</sub>	Source to Drain Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 20 A	-	_	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 20 A,	-	411	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_F/dt = 100 \text{ A/}\mu\text{s}$	-	7.8	_	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

## **TYPICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)



V<sub>DS</sub>, DRAIN-TO-SOURCE VOLTAGE (V)

Figure 5. Capacitance Characteristics

 $10^{-1}$ 

 $\label{eq:Qg} \textbf{Q}_g, \, \text{TOTAL GATE CHARGE (nC)}$  Figure 6. Gate Charge Characteristics

## TYPICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

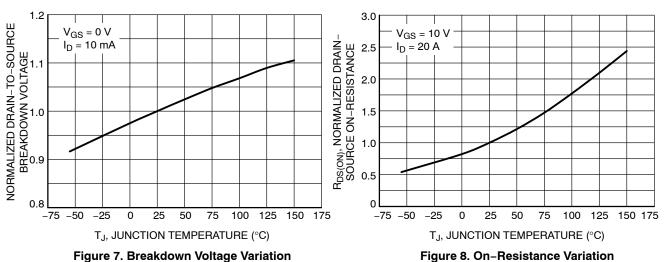


Figure 7. Breakdown Voltage Variation vs. Temperature

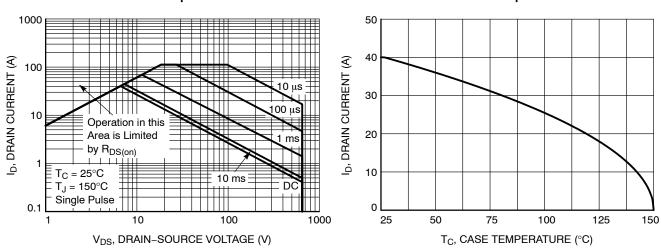


Figure 9. Maximum Safe Operating Area

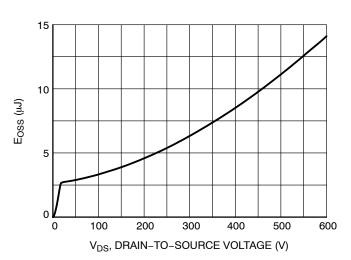


Figure 11. E<sub>OSS</sub> vs. Drain to Source Voltage

Figure 10. Maximum Drain Current vs. Case Temperature

vs. Temperature

# **TYPICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

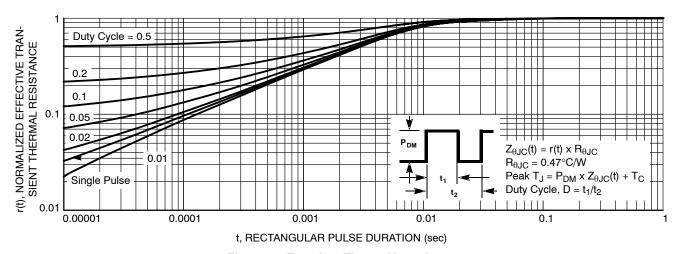


Figure 12. Transient Thermal Impedance

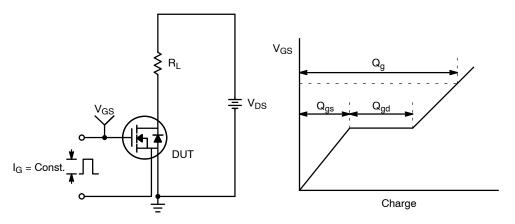


Figure 13. Gate Charge Test Circuit & Waveform

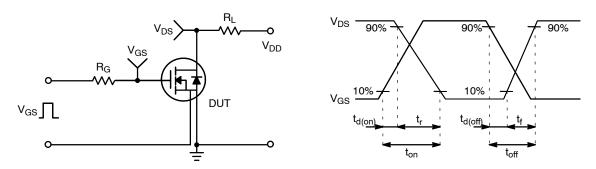


Figure 14. Resistive Switching Test Circuit & Waveforms

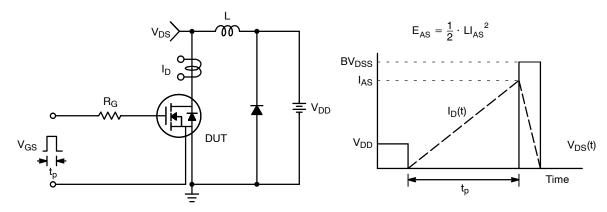


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

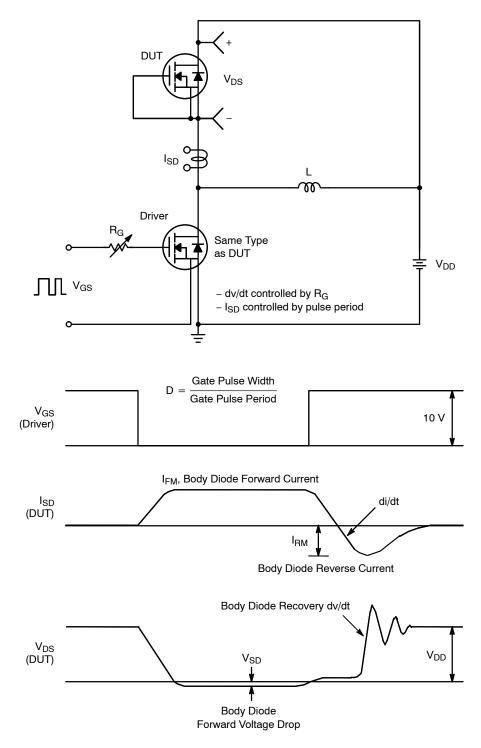
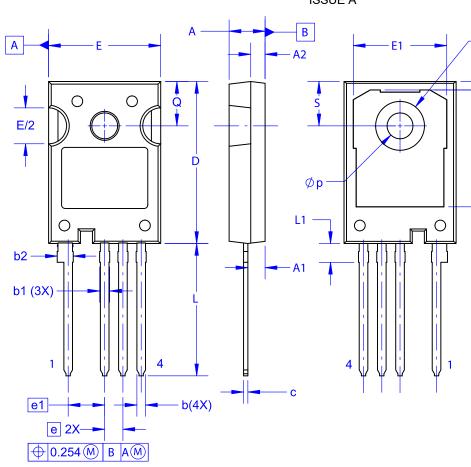


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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# PACKAGE DIMENSIONS

## TO-247-4LD CASE 340CJ ISSUE A



## NOTES:

- A. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
  B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD
  FLASH, AND TIE BAR EXTRUSIONS.
  C. ALL DIMENSIONS ARE IN MILLIMETERS.
  D. DRAWING CONFORMS TO ASME Y14.5-2009.

DIM	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	4.80	5.00	5.20		
A1	2.10	2.40	2.70		
A2	1.80	2.00	2.20		
b	1.07	1.20	1.33		
b1	1.20	1.40	1.60		
b2	2.02	2.22	2.42		
С	0.50	0.60	0.70		
D	22.34	22.54	22.74		
D1	16.00	16.25	16.50		
D2	0.97	1.17	1.37		
е	2	2.54 BSC			
e1	5	5.08 BSC			
Е	15.40	15.60	15.80		
E1	12.80	13.00	13.20		
E/2	4.80	5.00	5.20		
L	18.22	18.42	18.62		
L1	2.42 2.62		2.82		
р	3.40	3.60	3.80		
p1	6.60	6.80	7.00		
Q	5.97	6.17	6.37		
S	5.97	6.17	6.37		

Ø**p1** 

D1

- D2

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